



## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 6483

<b>SERIAL NUMBER</b> 09/895,559	<b>FILING DATE</b> 06/28/2001 <b>RULE</b>	<b>CLASS</b> 714	<b>GROUP ART UNIT</b> 2133	<b>ATTORNEY DOCKET NO.</b> JCLA4020
------------------------------------	---	---------------------	-------------------------------	--

## APPLICANTS

K. T. Hsiao, Taipei, TAIWAN;  
Hao-Liang Lo, Hsinchu, TAIWAN;  
Li-Yang Yang, Tainan Hsien, TAIWAN;

\*\* CONTINUING DATA \*\*\*\*\*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 08/17/2001

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY TAIWAN	SHEETS DRAWING 5	TOTAL CLAIMS 5	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature: <i>[Signature]</i> Initials: <i>[Initials]</i>				

## ADDRESS

J.C. Patents, Inc.  
Suite 114  
1340 Reynolds Ave.  
Irvine, CA 92614

## TITLE

Testing architecture for a semiconductor memory device

<b>FILING FEE RECEIVED</b> 710	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees ( Filing )
		<input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )
		<input type="checkbox"/> 1.18 Fees ( Issue )
		<input type="checkbox"/> Other _____
		<input type="checkbox"/> Credit